

IO-Link Gateway

UC2-IOL

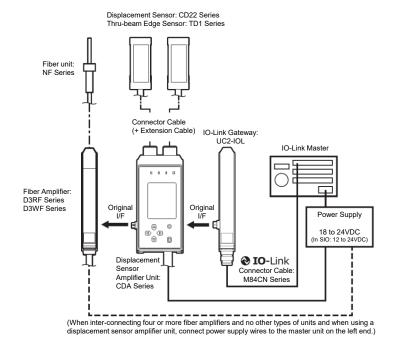
OPTEX FA CO. LTD.

The following operating procedure is for checking or changing setting values in a D3RF, D3WF, CDA, CD22 (via CDA) or TD1 (via CDA) connected to the UC2-IOL via IO-Link.

* Refer to the UC2-IOL address list or the address list of the connected module being configured.

General Description

IO-Link is used to connect the UC2-IOL to the IO-Link master unit, and a proprietary interface is used to connect the UC2-IOL to the connected modules (D3RF, etc.).



This means IO-Link should be used to check or change setting values in the connection module from the IO-Link master unit, and access should be through UC2-IOL module communication (address 262) and module data (address 263).

 263).
 * Refer to the UC2-IOL address list for more information on module communication (address 262) and module data (address 263).

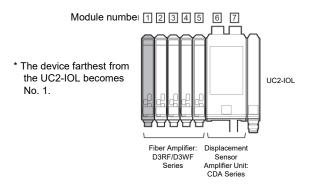
* Refer to the respective address list for the setting values and setting details of each connection module.

Operating Procedure

1. Check the number of the module to check or change the setting value.

Check the number of the module for which the setting value will be checked or changed. The modules are numbered (1, 2, 3, etc.) according to their distance from the UC2-IOL, starting with the farthest (leftmost side).

Example: With 5 fiber amplifiers and 1 displacement sensor amplifier unit connected



2. Check the module address and communication data length for the setting value to be checked or changed.

Refer to the address list of the module for which the setting value will be checked or changed, and check the module address and communication data length of the setting value.

Example:

To check the D3RF display setting	Module address: 100 Data length: 2
To perform D3WF first-point teaching	Module address: 202 Data length: 2
To check the CD22 near threshold	Module address: 130 Data length: 2

3. Set the module number, module address, and communication data length being checked or changed to the UC2-

IOL Configure the module number, module address, and communication data length in the UC2-IOL module communication settings (address: 262).

Example:

To check the display setting of the D3RF (Module number: 1)	Module number (address: 262, Sub-address: 1): 1 Module address (address: 262, Sub-address: 2): 100 Data length (address: 262, Sub-address: 3): 2
To perform first-point teaching for the D3WF (Module number: 2)	Module number (address: 262, Sub-address: 1): 2 Module address (address: 262, Sub-address: 2): 202 Data length (address: 262, Sub-address: 3): 2
To check the near threshold of the CD22 (Module number: 3)	Module number (address: 262, Sub-address: 1): 3 Module address (address: 262, Sub-address: 2): 130 Data length (address: 262, Sub-address: 3): 2

4. Check the setting value.

To check the setting value, read the UC2-IOL module data: data (address: 263, Sub-address: 1).

Example:

If the display setting for the D3RF (module number: 1) is set to bar display	0x00,0x01,0x00,0x00,0x00,0x00,0x00,0x00
If the near threshold of the CD22 (module number: 3) is -3.00	0xFE,0xD4,0x00,0x00,0x00,0x00,0x00,0x00,0x00

5. Change the setting value.

To change the setting value, write the target change value to UC2-IOL module data: data (address: 263, Sub-address: 1).

Example:

To perform D3WF (module number: 2) first-point teaching	0x00,0x01,0x00,0x00,0x00,0x00,0x00,0x00
To set the near threshold of the CD22 (module number: 3) to 12.34	0x04,0xD2,0x00,0x00,0x00,0x00,0x00,0x00,0x00

OPTEX FA CO., LTD.

[Headquarters]

91 Chudoji-Awata-cho Shimogyo-ku Kyoto 600-8815 JAPAN

TEL +81-75-325-1314 FAX +81-75-325-2936

Good Thinking, Good Future	INDEX LIST
IO-Link GATEWAY	
UCZ-IUL	
O-Link setting file (IODD file) can be downloaded from our web site.	🚷 IO-Link

Communication specifications

Min. cycle time	2.2 ms	
Baud rate	COM3 (230.4kbps)	
SDU support Yes		
IO-Link revision	1.1	
Inputting process data length	32	
Outputting process data length	0	
Vender ID	dec: 1076	hex: 0x0434
Device ID	dec: 86017	hex: 0x15001

Process Data Format

16 modules (13 analog outputs) (Index120: 0)

		unuiog	outputo					
Byte offset	0		1	2	3		4	5
Bit	7 6 5 4 3 2	1 0 7 6 5 4	321076	5 4 3 2 1 0	7 6 5 4 3 2	1 0 7 6 5 4	3 2 1 0 7 6	5 4 3 2 1 0
Name	Ar	nalog output 1		Analog	output 2		Analog outpu	t 3
Byte offset	6		7	8	9			11
Bit	7 6 5 4 3 2	1 0 7 6 5 4	321076		7 6 5 4 3 2	1 0 7 6 5 4		5 4 3 2 1 0
Name	Ai	nalog output 4		Analog	output 5		Analog outpu	t6
Byte offset	12		3	14	15		16	17
Bit	7 6 5 4 3 2	1 0 7 6 5 4		14 5 5 4 3 2 1 0				543210
Name		nalog output 7		Analog		107034	Analog outpu	
Byte offset	18	1	9	20	21	2	22	23
Bit	7 6 5 4 3 2	1 0 7 6 5 4	321076	5 4 3 2 1 0	7 6 5 4 3 2	1 0 7 6 5 4	3 2 1 0 7 6	5 4 3 2 1 0
Name	An	alog output 10		Analog	output 11		Analog output	12
Byte offset Bit	24	25 0765432	107654	3 2 1 0 7 6 5 4	27 3 2 1	0		
Name		g output 13	1 0 7 6 5 4 Rese			Module 16		
					Output 2	Output 1		
Byte offset				2	8			
Bit	7	6	5	4	3	2	1	0
Name	Module 15	Module 15	Module 14	Module 14	Module 13	Module 13	Module 12	Module 12
	Output 2	Output 1	Output 2	Output 1	Output 2	Output 1	Output 2	Output 1
Byte offset				2	9			
Bit	7	6	5	4	3	2	1	0
Name	Module 11 Output 2	Module 11 Output 1	Module 10 Output 2	Module 10 Output 1	Module 9 Output 2	Module 9 Output 1	Module 8 Output 2	Module 8 Output 1
Byte offset	-		-	3				
Bit	7	6	5	4	3	2	1	0
Name	Module 7 Output 2	Module 7 Output 1	Module 6 Output 2	Module 6 Output 1	Module 5 Output 2	Module 5 Output 1	Module 4 Output 2	Module 4 Output 1
Byte offset				3	1			
Bit	7	6	5	4	3	2	1	0
Name	Module 3 Output 2	Module 3 Output 1	Module 2 Output 2	Module 2 Output 1	Module 1 Output 2	Module 1 Output 1	Output 2	Output 1
	Output 2	Output i		Output i	U Output 2			1

Byte offset	0		1	2	3		4	5
Bit	7 6 5 4 3 2	10765	4 3 2 1 0	7 6 5 4 3 2 1	0 7 6 5 4 3 2	1 0 7 6 5 4	3 2 1 0 7 6	5 4 3 2 1
Name		Analog output 1		Analo	og output 2		Analog outpu	t 3
Byte offset	6		7	8	9		10	11
Bit	7 6 5 4 3 2	10765			0 7 6 5 4 3 2			5 4 3 2 1
Name		Analog output 4		Analo	g output 5		Analog outpu	t 6
Byte offset	12		13	14	15		16	17
Bit		10765	-		0765432			54321
Name		Analog output 7	+ 0 2 1 0		g output 8		Analog outpu	
					3			
Byte offset	18		19	20	21		22	23
Bit			4 3 2 1 0		0 7 6 5 4 3 2 g output 11	107654		5 4 3 2 1
Name	A 4	nalog output 10		Analo		Analog output	12	
Byte offset	24		25	26	27			
Bit	7 6 5 4 3 2	10765	4 3 2 1 0	7 6 5 4 3 2 1	0 7 6 5 4 3 2	1 0		
Name	A	nalog output 13		Analo	g output 14			
Byte offset		-			28			
Bit	7	6	5	4	3	2	1	0
Name	Re	serve	module 1		module 13	module 13	module 12	module 12
			Output 2	2 Output 1	Output 2	Output 1	Output 2	Output 1
Byte offset					29			
Bit	7	6	5	4	3	2	1	0
Name	module 11	module 11	module 1		module 9	module 9	module 8	module 8
	Output 2	Output 1	Output 2	2 Output 1	Output 2	Output 1	Output 2	Output 1
Byte offset	7	C		4	30			0
Bit	7 module 7	6 module 7	5 module	4 6 module 6	3 module 5	2 module 5	1 module 4	0 module 4
Name								

5 Module 2 Output 2

6 Module 3 Output 1

7 Module 3 Output 2

ame

4 Module 2 Output 1

3

Module 1 Output 2

2 Module 1 Output 1

1 Output 2

0 Output 1

Byte offset	0	1	2	3	4	5
Bit	7 6 5 4 3 2 1 0	7 6 5 4 3 2 1 0	7 6 5 4 3 2 1 0	7 6 5 4 3 2 1 0	7 6 5 4 3 2 1 0	7 6 5 4 3 2 1
Name	Analog	output 1	Analog	output 2	Analog	output 3
Byte offset	6	7	8	9	10	11
Bit	7 6 5 4 3 2 1 0	7 6 5 4 3 2 1 0	7 6 5 4 3 2 1 0	7 6 5 4 3 2 1 0	7 6 5 4 3 2 1 0	7 6 5 4 3 2 1
Name	Analog output 4		Analog	output 5	Analog	output 6
	· · · · · · · · · · · · · · · · · · ·				L	
Byte offset	12	13	14	15	16	17
Bit	7 6 5 4 3 2 1 0	7 6 5 4 3 2 1 0	7 6 5 4 3 2 1 0	7 6 5 4 3 2 1 0	7 6 5 4 3 2 1 0	7 6 5 4 3 2 1
Name	Analog	output 7	Analog	output 8	Analog	output 9
Byte offset		19	20	21	22	23
Bit	7 6 5 4 3 2 1 0	7 6 5 4 3 2 1 0	7 6 5 4 3 2 1 0	7 6 5 4 3 2 1 0		
Name	Analog	output 10	Analog	output 11	Analog o	output 12
Byte offset	24	25	26	27	28	29
Bit	7 6 5 4 3 2 1 0	7 6 5 4 3 2 1 0	7 6 5 4 3 2 1 0	7 6 5 4 3 2 1 0	7 6 5 4 3 2 1 0	7 6 5 4 3 2 1
Name	Analog	output 13	Analog	output 14	Analog o	output 15

		laiog outpu					
Byte offset	0	1	2	3	4	5	
Bit	7 6 5 4 3 2 1 0	7 6 5 4 3 2 1 0	7 6 5 4 3 2 1 0	7 6 5 4 3 2 1 0	7 6 5 4 3 2 1 0	7 6 5 4 3 2 1 0	
Name	Analog	output 1	Analog	output 2	Analog	output 3	
Byte offset	6	7	8	9	10	11	
Bit	7 6 5 4 3 2 1 0	7 6 5 4 3 2 1 0	7 6 5 4 3 2 1 0	7 6 5 4 3 2 1 0	7 6 5 4 3 2 1 0	7 6 5 4 3 2 1 0	
Name	Analog	output 4	Analog	output 5	Analog	output 6	
Byte offset	12	13	14	15	16	17	
Bit	7 6 5 4 3 2 1 0	7 6 5 4 3 2 1 0	7 6 5 4 3 2 1 0	7 6 5 4 3 2 1 0	7 6 5 4 3 2 1 0	7 6 5 4 3 2 1 0	
Name	Analog	output 7	Analog	output 8	Analog output 9		
Byte offset		19	20	21	22	23	
Bit	7 6 5 4 3 2 1 0	7 6 5 4 3 2 1 0			7 6 5 4 3 2 1 0	7 6 5 4 3 2 1 0	
Name	Analog o	output 10	Analog	output 11	Analog o	output 12	
	l						
Byte offset	24	25	26	27	28	29	
Bit	7 6 5 4 3 2 1 0	76543210		+ · · · · · · · ·	76543210	76543210	
Name		output 13		output 14			
INAITIE	Analog d	Julpul 13	Analog	output 14	Analog output 15		
	L		L		L		

Byte offset				3	0				
Bit	7	6	5	4	3	2	1	0	7
Name						Ar	nalo	og o	but

16 modules (16 analog outputs) (Index120: 2)

31 7 6 5 4 3 2 1 0 putput 16

Service Data

hex 0x0 0x1 0xC 0x10 0x12 0x13 0x14 0x15 0x16 0x17 0x18 0x24 0x25 0x28 0xD	Sub-in- dex No	Name Direct Parameters 1 Direct Parameters 2 Device Lock Data Storage Lock Vendor Name Product Name Product ID	Format (Offset) Record Record *3 Bit(1) String	Length 16 16 2 1	Access *1 R/W R/W R/W	Default value	Value/Range	Displa *2
0x1 0xC 0x10 0x12 0x13 0x14 0x15 0x16 0x17 0x18 0x24 0x25 0x28	- - 2 - - - - - - - -	Direct Parameters 2 Device Lock Data Storage Lock Vendor Name Product Name	Record Record *3 Bit(1)	16 2	R/W	-	-	
0xC 0x10 0x12 0x13 0x14 0x15 0x16 0x17 0x18 0x24 0x25 0x28	- 2 - - - - - - - - -	Device Lock Data Storage Lock Vendor Name Product Name	Record *3 Bit(1)	2	,	-	-	
0x10 0x12 0x13 0x14 0x15 0x16 0x17 0x18 0x24 0x25 0x28	- 2 - - - - - - - -	Data Storage Lock Vendor Name Product Name	Bit(1)		R/W	-		
0x12 0x13 0x14 0x15 0x16 0x17 0x18 0x24 0x25 0x28	2	Vendor Name Product Name		1				
0x12 0x13 0x14 0x15 0x16 0x17 0x18 0x24 0x25 0x28	- - - - - -	Product Name	String		R/W	0	0 : Unlock 1 : Lock	×
0x13 0x14 0x15 0x16 0x17 0x18 0x24 0x25 0x25	- - - - -		Lound	8	R	OPTEX FA	-	~
0x13 0x14 0x15 0x16 0x17 0x18 0x24 0x25 0x25	- - - -		String	7	R	UC2-IOL	-	~
0x15 0x16 0x17 0x18 0x24 0x25 0x28	-		String	5	R	20366	-	~
0x16 0x17 0x18 0x24 0x25 0x28	-	Product Text	String	45	R	Communication	-	~
0x16 0x17 0x18 0x24 0x25 0x28	- - -	0		10		gateway		
0x17 0x18 0x24 0x25 0x28	-	Serial Number	String	12	R	-	-	✓ ✓
0x18 0x24 0x25 0x28	-	Hardware Version	String	4	R	-	-	✓ ✓
0x24 0x25 0x28	-	Firmware Version	String	19 32	R	*****	-	✓ ✓
0x25 0x28		Application Specific Tag Device Status	String UInt	8	R/W R	0	- 0 : Device is OK	× ×
0x28	-	Device Status	Ont	0			4 : Failure	
	-	Detailed Device Status	Array *3	15	R	-	-	
0xD	-	Proess Data Input	PD In	16	R	-	-	
	-	Profile characteristic	Array *3	8	R	-	-	
0xE	-	PDInput descriptor	Array *3	18	R	-	-	
0xF	-	PDOutput Descriptor	Array *3	3	R	-	-	
0x3A	-	Teach-in channel	UInt	8bit	R/W	1	0 = default Qint. = Module 1 Qint.1 1 = Module 1 Qint.1 2 = Module 1 Qint.2 3 = Module 2 Qint.1 4 = Module 2 Qint.1 5 = Module 3 Qint.1 6 = Module 3 Qint.2 : 31 = Module 16 Qint.1 32 = Module 16 Qint.2 253 = All Qint.1	
							254 = All Qint.2	
0x3B	-	Teach-in state	Record *3	1	R	-		
	1	Teach flag SP2	Bit(6)	1bit	R	false	false = Not taught	
							true = Successfully taught	
	2	Teach flag SP1	Bit(4)	1bit	R	false	false = Not taught true = Successfully taught	
	3	Teach state	Bit(0)	4bit	R	0	0 = IDLE	
040		Device (Avertice serve	China		DAM		2 = SP2 SUCCESS 4 = WAIT FOR COMMAND 5 = BUSY 7 = ERROR	
	-				,		-	Ť
	1	User defined value for AO 1	Bit(104)	8bit	R/W	1	O : No function / empty : Current receiver level live Module 1 : Current receiver level live Module 2 : Current receiver level live Module 3 : : Sourcent receiver level live Module 15 16 : Current receiver level live Module 16	~
	2	User defined value for AO 2	Bit(96)	8bit	R/W	2	Ditto	~
	3	User defined value for AO 3	Bit(88)	8bit	R/W	3	Ditto	~
	4	User defined value for AO 4	Bit(80)	8bit	R/W	4	Ditto	~
	5	User defined value for AO 5	Bit(72)	8bit	R/W	5	Ditto	~
	6	User defined value for AO 6	Bit(64)	8bit	R/W	6	Ditto	~
	7	User defined value for AO 7	Bit(56)	8bit	R/W	7	Ditto	~
	8	User defined value for AO 8	Bit(48)	8bit	R/W	8	Ditto	~
	9	User defined value for AO 9		8bit	R/W	9	Ditto	~
	10	User defined value for AO 10	Bit(32)	8bit	R/W	10	Ditto	~
	11	User defined value for AO 11	Bit(24)	8bit	R/W	11	Ditto	~
	12	User defined value for AO 12	Bit(16)	8bit	R/W	12	Ditto	~
	13	User defined value for AO 13	Bit(8)	8bit	R/W	13	Ditto	~
	14	User defined value for AO 14	Bit(0)	8bit	R/W	14	Ditto	~
0x78	-	Process data select	UInt	8bit	R/W	0	0 : 13 AO out of 16 modules	~
							2 : 16 AO, no switching signals	
0x79	-	Pin 2 configuration	UInt	8bit	R/W	40	0 : Deactivated / no function 1 : External input (Smart Task) 17 : Teach-in 34 : Switching signal QL2 35 : Detection output Module 1 Qint.1 36 : Detection output Module 1 Qint.2 39 : Switching signal QL1 40 : Switching signal QL1/	~
	-	Temperature	Record *3	1	R	-	-	
0x99		Current temperature	Bit(0)	8bit	R	-	-127127 [°C]	~
0x99	1			4	R			
000)x40)x43	1 2 3 x40 - xx3 1 2 3 2 3 - x43 - 3 2 3 4 5 6 7 8 9 10 11 12 13 14 0x78	1 Teach flag SP2 2 Teach flag SP1 3 Teach state 3 Teach state 0x40 - 0x43 - 1 User defined value for AO 1 1 User defined value for AO 1 1 User defined value for AO 2 3 User defined value for AO 2 3 User defined value for AO 3 4 User defined value for AO 3 4 User defined value for AO 6 6 User defined value for AO 6 7 User defined value for AO 6 9 User defined value for AO 1 11 User defined value for AO 1 12 User defined value for AO 1 13 User defined value for AO 13 14 User defined value for AO 14 1X78 - Process data select	1 Teach flag SP2 Bit(6) 2 Teach flag SP1 Bit(4) 3 Teach state Bit(0) 3 Teach state Bit(0) 3 Process data user definition Record 1 User defined value for AO 1 Bit(104) 2 User defined value for AO 2 Bit(96) 3 User defined value for AO 2 Bit(96) 3 User defined value for AO 3 Bit(80) 5 User defined value for AO 4 Bit(80) 5 User defined value for AO 5 Bit(72) 6 User defined value for AO 6 Bit(64) 7 User defined value for AO 6 Bit(40) 10 User defined value for AO 9 Bit(40) 10 User defined value for AO 9 Bit(40) 11 User defined value for AO 1 Bit(22) 11 User defined value for AO 1 Bit(24) 12 User defined value for AO 1 Bit(24) 12 User defined value for AO 13 Bit(8) 14 User defined value for AO 14 Bit(0) 10 Process data select Uint	1 Teach flag SP2 Bit(6) 1 bit 2 Teach flag SP1 Bit(4) 1 bit 3 Teach state Bit(0) 4 bit 3 Teach state Bit(0) 4 bit 0x40 - Device / function name String 32 0x43 - Process data user definition Record 7 1 User defined value for AO 1 Bit(104) 8 bit 2 User defined value for AO 2 Bit(80) 8 bit 3 User defined value for AO 3 Bit(80) 8 bit 4 User defined value for AO 4 Bit(80) 8 bit 5 User defined value for AO 5 Bit(72) 8 bit 6 User defined value for AO 6 Bit(44) 8 bit 7 User defined value for AO 9 Bit(40) 8 bit 9 User defined value for AO 9 Bit(40) 8 bit 9 User defined value for AO 10 Bit(22) 8 bit 10 User defined value for AO 11 Bit(24) 8 bit 11 User defined value for AO 12 Bit(40) 8 bit 12 User defined value for AO 13 Bit(24) 8 bit 13 User defined value for AO 14 Bit(1 Teach flag SP2 Bit(6) 1 bit R 2 Teach flag SP1 Bit(4) 1 bit R 3 Teach state Bit(0) 4 bit R 3 Teach state Bit(0) 4 bit R 0x40 - Device / function name String 32 R/W 0x43 - Process data user definition Record 7 R/W 1 User defined value for AO 1 Bit(104) 8 bit R/W 2 User defined value for AO 2 Bit(96) 8 bit R/W 3 User defined value for AO 3 Bit(80) 8 bit R/W 4 User defined value for AO 4 Bit(80) 8 bit R/W 5 User defined value for AO 5 Bit(72) 8 bit R/W 6 User defined value for AO 6 Bit(40) 8 bit R/W 9 User defined value for AO 9 Bit(40) 8 bit R/W 10 User defined value for AO 9 Bit(40) 8 bit R/W 11 User defined value for AO 10 Bit(22	1 Teach flag SP2 Bit(6) 1 bit R false 2 Teach flag SP1 Bit(4) 1 bit R false 3 Teach state Bit(0) 4bit R 0 0x40 - Device / function name String 32 R/W ******* 0x43 - Process data user definition Record 7 R/W - 1 User defined value for AO 1 Bit(104) 8bit R/W 1 2 User defined value for AO 2 Bit(96) 8bit R/W 1 2 User defined value for AO 3 Bit(80) 8bit R/W 2 3 User defined value for AO 4 Bit(80) 8bit R/W 3 4 User defined value for AO 5 Bit(72) 8bit R/W 4 5 User defined value for AO 6 Bit(40) 8bit R/W 5 6 User defined value for AO 7 Bit(56) 8bit R/W 6 7 User defined value for AO 8 Bit(40) 8bit R/W 6<	kill kill

dec	x No.	Sub-in- dex No.	Name	Format (Offset)	Length	Access *1	Default value	Value/Range	Display *2
04	0xCC	-	Find me	UInt	8bit	R/W	0	0 : Find me deactivated	~
								1 : Find me activated	
19	0xDB	-	Article No. all components	Record	37	R	-	-	
		1	Article No. WI IO-Link Gateway	Bit(256)	5	R	20366	-	
		2	Article No. Module 1	Bit(240)	16bit	R	8449	8449 = D3RF (1 output)	
								8450 = D3RF (2 outputs)	
								8465 = D3WF (1 output)	
								8961 = CD22 (15mm model) on CDA	
								8962 = CD22 (35mm model) on CDA	
								8963 = CD22 (100mm model) on CDA	
								8993 = TD1 on CDA	
								9472 = Empty CDA channel	
								65535 = ERROR	
		3	Article No. Module 2	Bit(224)	16bit	R	8450	Ditto	
		4	Article No. Module 3	Bit(208)	16bit	R	8465	Ditto	
		5	Article No. Module 4	Bit(192)	16bit	R	8461	Ditto	
		6	Article No. Module 5	Bit(176)	16bit	R	8462	Ditto	
		7	Article No. Module 6	Bit(160)	16bit	R	8463	Ditto	
		8	Article No. Module 7	Bit(144)	16bit	R	8993	Ditto	
		9	Article No. Module 8	Bit(128)	16bit	R		Ditto	
		10	Article No. Module 9	Bit(112)	16bit	R		Ditto	
		11	Article No. Module 10	Bit(96)	16bit	R		Ditto	
		12	Article No. Module 11	Bit(80)	16bit	R		Ditto	
		13	Article No. Module 12	Bit(64)	16bit	R		Ditto	
		14	Article No. Module 13	Bit(48)	16bit	R		Ditto	
		15	Article No. Module 14	Bit(32)	16bit	R		Ditto	
		16	Article No. Module 15	Bit(16)	16bit	R		Ditto	
		17	Article No. Module 16	Bit(0)	16bit	R		Ditto	
26	0xE2	-	System state	Record *3	2	R	-		
		1	Input signal state Pin 2	Bit(7)	1bit	R	0	0 : External input LOW	~
							-	1 : External input HIGH	
27	0xE3	-	Notification handling	UInt	8bit	R/W	0	0 : All enabled	~
_,			literiteriteriteriteriteriteriteriteriter					1 : All disabled	
								2 : Events enabled, PD invalid flag	
								disabled	
								3 : Events disabled, PD invalid flag	
52	0x106		Module conmunication tunnel	Record	3	R/W		enabled	
		1	Module number	Bit(16)	8bit	R/W	1	1 : Module 1	
		l'		Bit(10)	ODIL	11/11		2 : Module 2	
								3 : Module 3	
								:	
								15 : Module 15	
				844.0				16 : Module 16	
		2	Module index	Bit(8)	8bit	R/W	0	-	
		3	Data length	Bit(0)	8bit	R/W	0	032	
~~	0.407	-	Module data	Record	35	R/W	-	-	
63	0x107	L					-	-	
63	0x107	1	Data	Bit(24)	32	R/W			
63	0x107	2	Data Module Number Readback	Bit(16)	8bit	R	-	-	
63	0x107	2	Data Module Number Readback Module Index Readback	Bit(16) Bit(8)	8bit 8bit	R R	-	-	
63	0×107	2	Data Module Number Readback	Bit(16)	8bit	R	-	- - -	
69	0×10D	2	Data Module Number Readback Module Index Readback Data Length Readback Module process data	Bit(16) Bit(8) Bit(0) Array	8bit 8bit 8bit 64	R R R R	-	- - - -	
69	0x10D 0x10E	2	Data Module Number Readback Module Index Readback Data Length Readback	Bit(16) Bit(8) Bit(0) Array UInt	8bit 8bit 8bit 64 8bit	R R R R R	-	- - - - 016	
69	0×10D	2	Data Module Number Readback Module Index Readback Data Length Readback Module process data	Bit(16) Bit(8) Bit(0) Array	8bit 8bit 8bit 64	R R R R			✓ ✓
69 70	0x10D 0x10E	2	Data Module Number Readback Module Index Readback Data Length Readback Module process data Module count	Bit(16) Bit(8) Bit(0) Array UInt	8bit 8bit 8bit 64 8bit	R R R R R	-		
69 70	0x10D 0x10E	2	Data Module Number Readback Module Index Readback Data Length Readback Module process data Module count	Bit(16) Bit(8) Bit(0) Array UInt	8bit 8bit 8bit 64 8bit	R R R R R	-	0 : Module 1 Qint.1	
69 70	0x10D 0x10E	2	Data Module Number Readback Module Index Readback Data Length Readback Module process data Module count	Bit(16) Bit(8) Bit(0) Array UInt	8bit 8bit 8bit 64 8bit	R R R R R	-	0 : Module 1 Qint.1 1 : Module 1 Qint.2	✓ ✓
69 70	0x10D 0x10E	2	Data Module Number Readback Module Index Readback Data Length Readback Module process data Module count	Bit(16) Bit(8) Bit(0) Array UInt	8bit 8bit 8bit 64 8bit	R R R R R	-	0 : Module 1 Qint.1 1 : Module 1 Qint.2 2 : Module 2 Qint.1	✓ ✓
69	0x10D 0x10E	2	Data Module Number Readback Module Index Readback Data Length Readback Module process data Module count	Bit(16) Bit(8) Bit(0) Array UInt	8bit 8bit 8bit 64 8bit	R R R R R	-	0 : Module 1 Qint. 1 1 : Module 1 Qint. 2 2 : Module 2 Qint. 1 3 : Module 2 Qint. 2	
69 70	0x10D 0x10E	2	Data Module Number Readback Module Index Readback Data Length Readback Module process data Module count	Bit(16) Bit(8) Bit(0) Array UInt	8bit 8bit 8bit 64 8bit	R R R R R	-	0 : Module 1 Qint.1 1 : Module 1 Qint.2 2 : Module 2 Qint.1 3 : Module 2 Qint.2 4 : Module 3 Qint.1	✓ ✓
69	0x10D 0x10E	2	Data Module Number Readback Module Index Readback Data Length Readback Module process data Module count	Bit(16) Bit(8) Bit(0) Array UInt	8bit 8bit 8bit 64 8bit	R R R R R	-	0 : Module 1 Qint.1 1 : Module 1 Qint.2 2 : Module 2 Qint.1 3 : Module 2 Qint.2 4 : Module 3 Qint.1 4 : Module 3 Qint.1	
69	0x10D 0x10E	2	Data Module Number Readback Module Index Readback Data Length Readback Module process data Module count	Bit(16) Bit(8) Bit(0) Array UInt	8bit 8bit 8bit 64 8bit	R R R R R	-	0 : Module 1 Qint.1 1 : Module 1 Qint.2 2 : Module 2 Qint.1 3 : Module 2 Qint.2 4 : Module 3 Qint.1 4 : Module 3 Qint.1 :	
69	0x10D 0x10E	2	Data Module Number Readback Module Index Readback Data Length Readback Module process data Module count	Bit(16) Bit(8) Bit(0) Array UInt	8bit 8bit 8bit 64 8bit	R R R R R	-	0 : Module 1 Qint.1 1 : Module 2 Qint.2 2 : Module 2 Qint.1 3 : Module 2 Qint.2 4 : Module 3 Qint.1 4 : Module 3 Qint.1 3 0 : Module 16 Qint.1 31 : Module 16 Qint.2	✓ ✓
69 70 081	0x10D 0x10E	2	Data Module Number Readback Module Index Readback Data Length Readback Module process data Module count Input selector 1	Bit(16) Bit(8) Bit(0) Array UInt	8bit 8bit 8bit 64 8bit	R R R R/W	-	0 : Module 1 Qint. 1 1 : Module 1 Qint.2 2 : Module 2 Qint.1 3 : Module 2 Qint.2 4 : Module 2 Qint.2 4 : Module 3 Qint.1 3 : Module 3 Qint.1 ; 30 : Module 16 Qint.1	✓
69 70 081	0x10D 0x10E 0x439 0x43A	2	Data Module Number Readback Module Index Readback Data Length Readback Module process data Module count Input selector 1	Bit(16) Bit(8) Bit(0) Array UInt UInt UInt	8bit 8bit 8bit 64 8bit 8bit 8bit	R R R R/W	- 0 0 64	0 : Module 1 Qint.1 1 : Module 2 Qint.1 3 : Module 2 Qint.2 4 : Module 3 Qint.2 4 : Module 3 Qint.1 3 : Module 3 Qint.1 3 0 : Module 16 Qint.1 31 : Module 16 Qint.2 64 : Ext.input 1 Ditto	· · ·
69 70 081	0x10D 0x10E 0x439	2	Data Module Number Readback Module Index Readback Data Length Readback Module process data Module count Input selector 1	Bit(16) Bit(8) Bit(0) Array UInt UInt	8bit 8bit 64 8bit 8bit	R R R R/W	0	0 : Module 1 Qint.1 1 : Module 1 Qint.2 2 : Module 2 Qint.1 3 : Module 2 Qint.2 4 : Module 3 Qint.1 4 : Module 3 Qint.1 30 : Module 16 Qint.1 31 : Module 16 Qint.2 64 : Ext.input 1 Ditto 0 : DIRECT	· · · · · · · · · · · · · · · · · · ·
69 70 081	0x10D 0x10E 0x439 0x43A	2	Data Module Number Readback Module Index Readback Data Length Readback Module process data Module count Input selector 1	Bit(16) Bit(8) Bit(0) Array UInt UInt UInt	8bit 8bit 8bit 64 8bit 8bit 8bit	R R R R/W	- 0 0 64	0 : Module 1 Qint.1 1 : Module 2 Qint.1 3 : Module 2 Qint.1 4 : Module 3 Qint.1 4 : Module 3 Qint.1 5 : Module 3 Qint.1 3 : Module 16 Qint.1 31 : Module 16 Qint.2 64 : Ext.input 1 Ditto 0 : DIRECT 1 : AND	· · · · · · · · · · · · · · · · · · ·
69 70 081	0x10D 0x10E 0x439 0x43A	2	Data Module Number Readback Module Index Readback Data Length Readback Module process data Module count Input selector 1	Bit(16) Bit(8) Bit(0) Array UInt UInt UInt	8bit 8bit 8bit 64 8bit 8bit 8bit	R R R R/W	- 0 0 64	0 : Module 1 Qint.1 1 : Module 1 Qint.2 2 : Module 2 Qint.1 3 : Module 2 Qint.2 4 : Module 3 Qint.1 4 : Module 3 Qint.1 30 : Module 16 Qint.1 31 : Module 16 Qint.2 64 : Ext.input 1 Ditto 0 : DIRECT 1 : AND 2 : OR	· · · · · · · · · · · · · · · · · · ·
69 70 081	0x10D 0x10E 0x439 0x43A	2	Data Module Number Readback Module Index Readback Data Length Readback Module process data Module count Input selector 1	Bit(16) Bit(8) Bit(0) Array UInt UInt UInt	8bit 8bit 8bit 64 8bit 8bit 8bit	R R R R/W	- 0 0 64	0 : Module 1 Qint.1 1 : Module 2 Qint.1 2 : Module 2 Qint.1 3 : Module 2 Qint.2 4 : Module 3 Qint.2 4 : Module 3 Qint.1 3 : Module 16 Qint.1 31 : Module 16 Qint.2 64 : Ext.input 1 Ditto 0 : DIRECT 1 : AND 2 : OR 3 : Window Mode	· · · · · · · · · · · · · · · · · · ·
69 70 081	0x10D 0x10E 0x439 0x43A 0x43A	2	Data Module Number Readback Module Index Readback Data Length Readback Module process data Module count Input selector 1 Input selector 2 Logic 1	Bit(16) Bit(8) Bit(0) Array Ulnt Ulnt Ulnt Ulnt	Bbit Bbit Bbit 64 Bbit Bbit Bbit Bbit	R R R R R/W R/W	- 0 0 64 0	0 : Module 1 Qint.1 1 : Module 2 Qint.2 2 : Module 2 Qint.1 3 : Module 2 Qint.2 4 : Module 3 Qint.1 4 : Module 3 Qint.1 3 0 : Module 16 Qint.1 31 : Module 16 Qint.2 64 : Ext.input 1 Ditto 0 : DIRECT 1 : AND 2 : OR 3 : Window Mode 4 : Hysteresis	×
69 70 081 082 082 083	0x10D 0x10E 0x439 0x43A 0x43B	2	Data Module Number Readback Module Index Readback Module process data Module count Input selector 1 Input selector 2 Logic 1 Logic 2	Bit(16) Bit(8) Bit(0) Array UInt UInt UInt UInt UInt	Bbit Bbit Bbit 64 Bbit Bbit Bbit Bbit Bbit	R R R R B/W B/W	- 0 64 0	0 : Module 1 Qint.1 1 : Module 2 Qint.1 3 : Module 2 Qint.2 4 : Module 2 Qint.2 4 : Module 3 Qint.1 4 : Module 3 Qint.1 3 0 : Module 16 Qint.1 31 : Module 16 Qint.2 64 : Ext.input 1 Ditto 0 : DIRECT 1 : AND 2 : OR 3 : Window Mode 4 : Hysteresis Ditto	· · · · · · · · · · · · · · · · · · ·
69 70 081 082 083	0x10D 0x10E 0x439 0x43A 0x43A	2	Data Module Number Readback Module Index Readback Data Length Readback Module process data Module count Input selector 1 Input selector 2 Logic 1	Bit(16) Bit(8) Bit(0) Array Ulnt Ulnt Ulnt Ulnt	Bbit Bbit Bbit 64 Bbit Bbit Bbit Bbit	R R R R B/W B/W	- 0 0 64 0	0 : Module 1 Qint.1 1 : Module 2 Qint.1 3 : Module 2 Qint.2 4 : Module 2 Qint.2 4 : Module 3 Qint.1 3 : Module 3 Qint.1 3 : Module 16 Qint.1 31 : Module 16 Qint.2 64 : Ext.input 1 Ditto 0 : DIRECT 1 : AND 2 : OR 3 : Window Mode 4 : Hysteresis Ditto 0 : deactivated	· · · · · · · · · · · · · · · · · · ·
69 70 081 082 082 083	0x10D 0x10E 0x439 0x43A 0x43B	2	Data Module Number Readback Module Index Readback Module process data Module count Input selector 1 Input selector 2 Logic 1 Logic 2	Bit(16) Bit(8) Bit(0) Array UInt UInt UInt UInt UInt	Bbit Bbit Bbit 64 Bbit Bbit Bbit Bbit Bbit	R R R R B/W B/W	- 0 64 0	0 : Module 1 Qint.1 1 : Module 2 Qint.1 3 : Module 2 Qint.2 4 : Module 3 Qint.2 4 : Module 3 Qint.1 3 : Module 3 Qint.1 3 : Module 16 Qint.1 31 : Module 16 Qint.2 64 : Ext.input 1 Ditto 0 : DIRECT 1 : AND 2 : OR 3 : Window Mode 4 : Hysteresis Ditto 0 : deactivated 1 : T-on delay	· · · · · · · · · · · · · · · · · · ·
69 70 081 082 082 083	0x10D 0x10E 0x439 0x43A 0x43B	2	Data Module Number Readback Module Index Readback Module process data Module count Input selector 1 Input selector 2 Logic 1 Logic 2	Bit(16) Bit(8) Bit(0) Array UInt UInt UInt UInt UInt	Bbit Bbit Bbit 64 Bbit Bbit Bbit Bbit Bbit	R R R R B/W B/W	- 0 64 0	0 : Module 1 Qint. 1 1 : Module 2 Qint. 1 3 : Module 2 Qint. 2 4 : Module 3 Qint. 2 4 : Module 3 Qint. 1 3 : Module 3 Qint. 1 3 : Module 16 Qint. 1 3 : Module 16 Qint. 2 64 : Ext.input 1 Ditto 0 : DIRECT 1 : AND 2 : OR 3 : Window Mode 4 : Hysteresis Ditto 0 : deactivated 1 : T-on delay 2 : T-off delay	· · · · · · · · · · · · · · · · · · ·
69 70 081 082 082 083	0x10D 0x10E 0x439 0x43A 0x43B	2	Data Module Number Readback Module Index Readback Module process data Module count Input selector 1 Input selector 2 Logic 1 Logic 2	Bit(16) Bit(8) Bit(0) Array UInt UInt UInt UInt UInt	Bbit Bbit Bbit 64 Bbit Bbit Bbit Bbit Bbit	R R R R B/W B/W	- 0 64 0	0 : Module 1 Qint.1 1 : Module 2 Qint.1 3 : Module 2 Qint.2 4 : Module 2 Qint.2 4 : Module 3 Qint.1 3 : Module 3 Qint.1 3 : Module 16 Qint.1 31 : Module 16 Qint.2 64 : Ext.input 1 Ditto 0 : DIRECT 1 : AND 2 : OR 3 : Window Mode 4 : Hysteresis Ditto 0 : deactivated 1 : T-on delay 2 : T-off delay 3 : T-on/T-off delay	· · · · · · · · · · · · · · · · · · ·
69 70 081 082 083 083 084 085	0x10D 0x10E 0x439 0x43A 0x43B 0x43D	2	Data Module Number Readback Module Index Readback Module process data Module count Input selector 1 Input selector 2 Logic 1 Logic 2 Tiber 1 Mode	Bit(16) Bit(8) Bit(0) Array Ulnt Ulnt Ulnt Ulnt Ulnt Ulnt Ulnt	Bbit Bbit Bbit 64 Bbit Bbit Bbit Bbit Bbit	R R R R/W R/W R/W	- 0 0 64 0 0	0 : Module 1 Qint. 1 1 : Module 2 Qint. 1 3 : Module 2 Qint. 2 4 : Module 3 Qint. 2 4 : Module 3 Qint. 1 3 : Module 3 Qint. 1 3 : Module 16 Qint. 1 3 : Module 16 Qint. 2 64 : Ext.input 1 Ditto 0 : DIRECT 1 : AND 2 : OR 3 : Window Mode 4 : Hysteresis Ditto 0 : deactivated 1 : T-on delay 2 : T-off delay	· · · · · · · · · · · · · · · · · · ·
082 083 084 085	0x10D 0x10E 0x439 0x43A 0x43B	2	Data Module Number Readback Module Index Readback Module process data Module count Input selector 1 Input selector 2 Logic 1 Logic 2	Bit(16) Bit(8) Bit(0) Array UInt UInt UInt UInt UInt	Bbit Bbit Bbit 64 Bbit Bbit Bbit Bbit Bbit	R R R R B/W B/W	- 0 64 0	0 : Module 1 Qint.1 1 : Module 2 Qint.1 3 : Module 2 Qint.2 4 : Module 2 Qint.2 4 : Module 3 Qint.1 3 : Module 3 Qint.1 3 : Module 16 Qint.1 31 : Module 16 Qint.2 64 : Ext.input 1 Ditto 0 : DIRECT 1 : AND 2 : OR 3 : Window Mode 4 : Hysteresis Ditto 0 : deactivated 1 : T-on delay 2 : T-off delay 3 : T-on/T-off delay	· · · · · · · · · · · · · · · · · · ·
69 70 081 082 083 084 085	0x10D 0x10E 0x439 0x43A 0x43B 0x43D	2	Data Module Number Readback Module Index Readback Module process data Module count Input selector 1 Input selector 2 Logic 1 Logic 2 Tiber 1 Mode	Bit(16) Bit(8) Bit(0) Array Ulnt Ulnt Ulnt Ulnt Ulnt Ulnt Ulnt	8bit 8bit 8bit 8bit 8bit 8bit 8bit 8bit 8bit	R R R R/W R/W R/W	- 0 0 64 0 0	0 : Module 1 Qint.1 1 : Module 2 Qint.2 2 : Module 2 Qint.2 3 : Module 2 Qint.2 4 : Module 3 Qint.1 4 : Module 3 Qint.1 3 0 : Module 16 Qint.1 31 : Module 16 Qint.2 64 : Ext.input 1 Ditto 0 : DIRECT 1 : AND 2 : OR 3 : Window Mode 4 : Hysteresis Ditto 0 : deactivated 1 : T-on delay 2 : T-off delay 4 : Impulse (one shot)	· · · · · · · · · · · · · · · · · · ·
69 70 081 082 082 083 084 085	0x10D 0x10E 0x439 0x439 0x438 0x438 0x432 0x432	2	Data Module Number Readback Module Index Readback Module process data Module count Input selector 1 Input selector 2 Logic 1 Logic 2 Tiber 1 Mode Timer 2 Mode	Bit(16) Bit(8) Bit(0) Array Ulnt Ulnt Ulnt Ulnt Ulnt Ulnt Ulnt Ulnt	8bit 8bit	R R R R R/W R/W R/W R/W R/W	- 0 0 64 0 0 0 0	0 : Module 1 Qint.1 1 : Module 2 Qint.2 2 : Module 2 Qint.2 3 : Module 2 Qint.2 4 : Module 3 Qint.1 4 : Module 3 Qint.1 3 : Module 16 Qint.1 31 : Module 16 Qint.2 64 : Ext.input 1 Ditto 0 : DIRECT 1 : AND 2 : OR 3 : Window Mode 4 : Hysteresis Ditto 0 : deactivated 1 : T-on delay 2 : T-off delay 4 : Impulse (one shot) Ditto	· · · · · · · · · · · · · · · · · · ·
69 70 081 082 083 083 083 084 085 086 087 088	0x10D 0x10E 0x439 0x439 0x438 0x438 0x438 0x438	2	Data Module Number Readback Module Index Readback Module process data Module count Input selector 1 Input selector 2 Logic 1 Logic 2 Tiber 1 Mode Timer 2 Mode Timer 1 Setup	Bit(16) Bit(8) Bit(0) Array Ulnt Ulnt Ulnt Ulnt Ulnt Ulnt Ulnt Ulnt	8bit	R R R R B/W R/W R/W R/W R/W R/W R/W	- 0 0 64 0 0 0 0 1[ms]	0 : Module 1 Qint.1 1 : Module 2 Qint.1 3 : Module 2 Qint.2 4 : Module 2 Qint.2 4 : Module 3 Qint.1 4 : Module 3 Qint.1 3 : Module 16 Qint.1 3 : Module 16 Qint.2 64 : Ext.input 1 Ditto 0 : DIRECT 1 : AND 2 : OR 3 : Window Mode 4 : Hysteresis Ditto 0 : deactivated 1 : T-on delay 2 : T-onf delay 3 : T-on/T-off delay 4 : Impulse (one shot) Ditto 130000 [ms]	· · · · · · · · · · · · · · · · · · ·
63 69 70 081 082 083 084 085 086 087 088 089	0x10D 0x10E 0x439 0x439 0x438 0x438 0x430 0x430 0x435 0x435	2	Data Module Number Readback Module Index Readback Module process data Module count Input selector 1 Input selector 2 Logic 1 Logic 2 Tiber 1 Mode Timer 2 Mode Timer 2 Setup	Bit(16) Bit(8) Bit(0) Array Ulnt Ulnt Ulnt Ulnt Ulnt Ulnt Ulnt Ulnt	8bit 8bit	R R R R B/W R/W R/W R/W R/W R/W R/W R/W R/W R/W R/W	- 0 0 64 0 0 0 0 1[ms] 1[ms]	0 : Module 1 Qint.1 1 : Module 2 Qint.1 3 : Module 2 Qint.2 4 : Module 2 Qint.2 4 : Module 3 Qint.1 3 : Module 3 Qint.1 3 : Module 16 Qint.1 31 : Module 16 Qint.2 64 : Ext.input 1 Ditto 0 : DIRECT 1 : AND 2 : OR 3 : Window Mode 4 : Hysteresis Ditto 0 : deactivated 1 : T-on delay 2 : T-on/T-off delay 4 : Impulse (one shot) Ditto 130000 [ms] 130000 [ms]	
69 70 081 082 083 083 084 085 086 087 088	0x10D 0x10E 0x439 0x439 0x438 0x438 0x430 0x430 0x435 0x435	2	Data Module Number Readback Module Index Readback Module process data Module count Input selector 1 Input selector 2 Logic 1 Logic 2 Tiber 1 Mode Timer 2 Mode Timer 2 Setup	Bit(16) Bit(8) Bit(0) Array Ulnt Ulnt Ulnt Ulnt Ulnt Ulnt Ulnt Ulnt	8bit 8bit	R R R R R/W R/W R/W R/W R/W R/W R/W R/W	- 0 0 64 0 0 0 0 1[ms] 1[ms]	0 : Module 1 Qint.1 1 : Module 2 Qint.1 3 : Module 2 Qint.2 4 : Module 3 Qint.2 4 : Module 3 Qint.1 3 : Module 16 Qint.1 3 : Module 16 Qint.2 64 : Ext.input 1 Ditto 0 : DIRECT 1 : AND 2 : OR 3 : Window Mode 4 : Hysteresis Ditto 0 : deactivated 1 : T-on delay 2 : T-off delay 3 : T-off delay 4 : Impulse (one shot) Ditto 130000 [ms] 0 : not inverted	· · · · · · · · · · · · · · · · · · ·

Events

C	ode	Name	Turne	Description		
dec hex		Name	Туре	Description		
16912	0x4210	Device temperature over-run	Warning	Clear source of heat		
16928	0x4220	Device temperature under-run	Warning	Insulate device		
6144	0x1800	Short Circuit on Qx	Notification	Short circuit on outputs		
6145	0x1801	Teach / value out of specified range	Notification	Teach / distance value out of specified range		
6146	0x1802	Invalid module order	Error	Module 116 must follow the specified order (e.g. D3RF – D3WF – CDA)		
6147	0x1803	Module count changed	Error	Module count has changed since last power-up of the gateway device.		
6148	0x1804	Invalid module connected	Error	An invalid/not compatible module has been connected to the gateway.		
6149 0x1805 Module communication fault Notification Fa		Fault detected in communication between modules				

Errors

C	ode	de Additional code		Name	Description					
dec	hex	dec hex		Name	Description					
128	0x80	17	0x11	Index not available	Access occurs to a not existing index					
128	0x80	18	0x12	Subindex not available	Access occurs to a not existing subindex					
128	0x80	32	0x20	Service temporarily not available	Parameter is not accessible due to the current state of the device application					
128	0x80	35	0x23	Access denied	Write access on a read-only parameter					
128	0x80	48	0x30	Parameter value out of range	Written parameter value is outside its permitted value range					
128	0x80	51	0x33	Parameter length overrun	Written parameter length is above its predefined length					
128	0x80	52	0x34	Parameter length underrun	Written parameter length is below its predefined length					
128	0x80	64	0x40	Invalid parameter set	Written single parameter collides with other actual parameter settings					
128	0x80	65	0x41	Inconsistent parameter set	Parameter inconsistencies were found at the end of block parameter transfer, device plausibility check failed					

System command

Index No.		Cub index No		Nama	Value (Damas	Disalau t0	Defaulturatura
dec	hex	Sub-index No.	Access	Name	Value/Range	Display *2	Default value
2	0x2	-	w		65: SP1 Teach 66: SP2 Teach 130: Restore Factory Settings		-

*2. Supports the display function of OPTEX FA IO-Link master.

OPTEX FA CO.,LTD.

[Headquarters] 91 Chudoji-Awata-cho Shimogyo-ku Kyoto 600-8815 JAPAN TEL +81-75-325-1314 FAX +81-75-325-2936



Compact Laser Displacement Sensor

CD22 Series

OPTEX FA CO. LTD.

Module Address

Address		Name	Access	Format	Length	Value/Range	Address for
Dec	hex		*1	(Offset)	(Byte)		CDA *2
1	0x1	Product Type	RO	UINT	2	0x0011	
2	0x2	Firmware Version	RO	UINT	2	0x0011	
3	0x3	Protocol Version	RO	UINT	2	0x0000	
4	0x4	Product Revision	RO	UINT	2	0x0000	
5	0x5	Vendor Name	RO	STRING	2 to 16	"OPTEX FA"	
6	0x6	Product Name	RO	STRING	2 to 32	Product name	
7	0x7	Product ID	RO	STRING	2 to 32	Product ID	
8	0x8	User Tag Name (Max. 32 bytes)	R/W	STRING	2 to 32	88	
						0: Initialize	
9	0x9	Operation Status	RO	UINT	2	1: Idle 2: Execute 3: For use by user	
98	0x62	Error Status Refer to the separate "Errors" table.	R/W	UINT	2	Read: Last error code Write arbitrary value: Delete last error code	
100	0x64	Threshold Near	R/W	UINT	2		
101	0x65	Threshold Far	R/W	UINT	2	-32768~32767	· ·
102	0x66	Output hysteresis	R/W	UINT	2	0~32767	~
						0: No calculation	
103	0x67	Calculation flags	R/W	UINT	2	1: With calculation	· ·
104	0x68	Calculation coefficient Add (Setting value for each connected sensor)	R/W	UINT	2		
105	0x69	Calculation coefficient Multiply (Setting value for each connected	R/W	UINT	2	-10000~10000	~
106	0x6A	sensor) Calculation coefficient Divide (Setting value for each connected	R/W	UINT	2	1~32767	~
		sensor)				0: Sensor 2 measured value	-
107	0x6B	Monitor Calculation Values	R/W R/W	UINT	2	1: Calculated value	~
	0x6C	Calculation Values Head1			2	0: None	
109	0x6D	Calculation Values Head2	R/W	UINT	2	1: Addition	· ·
110	0x6E	Calculation Values Unit left Head1	R/W	UINT	2	2: Subtraction	
111	0x6F	Calculation Values Unit left Head2	R/W	UINT	2	3: Absolute value difference	
112	0x70	I/O polarity	R/W	UINT	2	0:PNP (N.O.) 1:NPN (N.O.) 2:PNP (N.C.) 3:NPN (N.C.)	v
113	0x71	Output Selection CH1	R/W	UINT	2	0: No output	
114	0x72	Output Selection CH2	R/W	UINT	2	1: Calculated value within threshold range (GO) 2: Calculated value out of range on near side (LO)	
115	0x73	Output Selection CH3	R/W	UINT	2	3: Calculated value out of range on far side (HI) 4: Sensor 1 with intershold range (GO) 5: Sensor 1 out of range on near side (LO) 6: Sensor 1 out of range on far side (HI) 7: Sensor 2 within threshold range (GO) 8: Sensor 2 out of range on near side (IO) 9: Sensor 2 out of range on near side (HI)	v
116	0x74	External input selection	R/W	UINT	2	5. Seriou 2 out of range of rail side (n) 6. No function 1. Teaching (Far side on rising edge, near side on falling edge) 2. BGS/FGS teaching 3. Zero reset 4. Laser emission OFF	~
117	0x75	Analog output selection	R/W	UINT	2	0: None 1: Calculated value 2: Sensor 1 3: Sensor 2	~
118	0x76	Analog scaling	R/W	UINT	2	0: No scaling 1: Scaling	~
119 120	0x77 0x78	Analog Scaling (max.) 10 V /20 mA Analog Scaling (min.) 0 V / 4 mA	R/W R/W	UINT UINT	2	-32768~32767	~
121	0x79	Baud rate (Setting value for each connected sensor)	R/W	UINT	2	0: No connection 1: 9.6 kbps 2: 19.2 kbps 3: 38.4 kbps 4: 57.6 kbps 5: 115.2 kbps 6: 230.4 kbps 7: 312.5 kbps 4:488.75 kbps 9: 500 kbps 10: 625 kbps 11: 633.3 kbps 11: 633.3 kbps 12: 937.5 kbps	~
400	0.04	A secolitica a secolo se a se da	D 0	LUNT		13: 1250 kbps	-
129	0x81	Amplifier product code	RO	UINT	2	0x2503	· ·
130	0x82	Switching point Near (Leap point Close)	R/W	UINT	2	CD22-15-485: -7499 to 7499 (in units of 1 µm)	
131	0x83	Switching point Far (Leap point Away)	R/W	UINT	2	CD22-35-485: -2249 to 2249 (in units of 10 µm)	
132	0x84	Background ObSB	R/W	UINT	2	CD22-100-485: -7499 to 7499 (in units of 1 µm)	_
133	0x85	Tolerance ObSB	R/W	UINT	2	CD22-15-485: 0 to 7499 (in units of 1 µm) CD22-35-485: 0 to 2249 (in units of 10 µm) CD22-100-485: 0 to 7499 (in units of 1 µm)	

Address No.		Name	Access	Format	Length	Value/Range	Address for
lec	hex	Name -	*1	(Offset)	(Byte)	Valder Calige	CDA *2
134	0x86	Average Number	R/W	UINT	2	0: 1 time 1: 8 times 2: 64 times 3: 512 times	
135	0x87	Teaching mode	R/W	UINT	2	0: 2-point teaching 1: 1-point teaching 2: FGS2	
136	0x88	Sampling rate	R/W	UINT	2	0:500 µs 1:1ms 2:2ms 3:4ms 4:AUTO	
137	0x89	Keylock	R/W	UINT	2	0: No locking 1: Locking	
138	0x8A	Switching behavior	R/W	UINT	2	0: Light ON (ON within range) 1: Dark ON (ON outside of range)	
139	0x8B	Calibration distance Close	R/W	UINT	2	CD22-15-485: -7499 to 7499 (in units of 1 µm)	
140	0x8C	Calibration distance Away	R/W	UINT	2	CD22-35-485: -2249 to 2249 (in units of 10 µm) CD22-100-485: -7499 to 7499 (in units of 1 µm)	
141	0x8D	Error Behavior	R/W	UINT	2	0: Output clamp error value (H7FFF) 1: Maintain the effective measured value immediately before hold	
142	0x8E	Clamp holding time	R/W	UINT	2	0~9999	
144	0x90	Zeroing Value	RO	UINT	2	CD22-15-485: -7499 to 7499 (in units of 1 µm) CD22-35-485: -2249 to 2249 (in units of 10 µm) CD22-100-485: -7499 to 7499 (in units of 1 µm)	
148	0x94	Barycenter	R/W	UINT	2	0: Waveform with highest received light level 1: Point closest to sensor 2: Second-closest point to sensor 3: Third-closest point to sensor 4: Fourth-closest point to sensor 5: Fifth-closest point to sensor	
151	0x97	Hysteresis value	R/W	UINT	2	CD22-15-485: 0 to 7499 (in units of 1 µm) CD22-35-485: 0 to 2249 (in units of 10 µm) CD22-100-485: 0 to 7499 (in units of 1 µm)	
152	0x98	Sensitivity	R/W	UINT	2	0: Automatic adjustment 1: Lowest 2: Low 3: Medum 4: Medium-high 5: High 6: Highest	
153	0x99	Light threshold	R/W	UINT	2	0: Lowest surface 1: Low position 2: Middle position 3: High position	
155	0x9B	Keylock active	R/W	UINT	2	0: Display when locked 1: Off when locked	
200		Save Zero Reposition	WO	Byte	2	Execute with written value	
201	0xC9	Cancel zero-reset	WO	Byte	2	Execute with written value	
202		Teach in 1 Point Teach in 2 Point	WO WO	UINT	2	Execute with written value Execute with written value	
203		Teach in 2 Point	wo	UINT	2	Execute with written value	+
210	0xD2	Warm start Reset	wo	UINT	2	3: Restart	-
211		Return to top menu	wo	Byte	2	0, 1, 2: Execute	1
212	0xD4	Factory Reset	wo	UINT	2	3: Initialize	

*1. RO=Read Only, R/W=Read/Write, WO=Write Only *2. Displacement Sensor Amplifier Unit

Errors

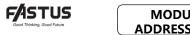
High-order byte	High-order byte			Description					
dec	hex	dec	hex						
0	0 0x0 0 0x0		0x0	No error					
		1	0x1	The address number is out of range.					
		2	0x2	The sub address number is not supported.					
		3	0x3	The ETX code in the command string is invalid.					
		4	0x4	A checksum error was detected.					
Applicable add	ress number	5	0x5	The command code is invalid.					
		6	0x6	A non-regulated parameter was specified.					
		7	0x7	An out of range numeric value was specified.					
		8	0x8	Reserved					
		9	0x9	Reserved					

OPTEX FA CO., LTD.

[Headquarters]

91 Chudoji-Awata-cho Shimogyo-ku Kyoto 600-8815 JAPAN

TEL +81-75-325-1314 FAX +81-75-325-2936



Displacement Sensor Amplifier Unit

CDA Series

OPTEX FA CO. LTD.

Module Address

Address No.				Format	Length				
lec	hex	Name	Access*	(Offset)	(Byte)	Value/Range			
100	0x64	Threshold Near	R/W	UINT	2				
101	0x65	Threshold Far	R/W	UINT	2	-32768~32767			
102	0x66	Output hysteresis	R/W	UINT	2	0~32767			
103	0x67	Calculation flags	R/W	UINT	2	0: No calculation 1: With calculation			
104	0x68	Calculation coefficient Add (Setting value for each connected sensor)	R/W	UINT	2	-10000~10000			
105	0x69	Calculation coefficient Multiply (Setting value for each connected sensor)	R/W	UINT	2	-1000~10000			
106	0x6A	Calculation coefficient Divide (Setting value for each connected sensor)	R/W	UINT	2	1~32767			
107	0x6B	Monitor Calculation Values	R/W	UINT	2	0: Sensor 2 measured value 1: Calculated value			
108	0x6C	Calculation Values Head1	R/W	UINT	2	0: None			
109	0x6D	Calculation Values Head2	R/W	UINT	2	1: Addition			
110	0x6E	Calculation Values Unit left Head1	R/W	UINT	2	2: Subtraction			
111	0x6F	Calculation Values Unit left Head2	R/W	UINT	2	3: Absolute value difference			
112	0x70	I/O polarity	R/W	UINT	2	0:PNP (N.O.) 1:NPN (N.O.) 2:PNP (N.C.) 3:NPN (N.C.)			
113	0x71	Output Selection CH1	R/W	UINT	2	0: No output 1: Calculated value within threshold range (GO)			
114	0x72	Output Selection CH2	R/W	UINT	2	2: Calculated value out of range on near side (LO) 3: Calculated value out of range on far side (HI) 4: Senser 1 within threshold range (CO)			
115	0x73	Output Selection CH3	R/W	UINT	2	Ersor 1 within threshold range (GO) Sensor 1 out of range on near side (LO) Sensor 1 out of range on far side (HI) Sensor 2 within threshold range (GO) Sensor 2 out of range on near side (LO) Sensor 2 out of range on far side (HI)			
116	0x74	External input selection	R/W	UINT	2	0: No function 1: Teaching (Far side on rising edge, near side on falling edge) 2: BCS/FGS teaching 3: Zero reset 4: Laser emission OFF			
117	0x75	Analog output selection	R/W	UINT	2	0: None 1: Calculated value 2: Sensor 1 3: Sensor 2			
118	0x76	Analog scaling	R/W	UINT	2	0: No scaling 1: Scaling			
119	0x77	Analog Scaling (max.) 10 V /20 mA	R/W	UINT	2	-32768~32767			
120	0x78	Analog Scaling (min.) 0 V / 4 mA	R/W	UINT	2				
121	0x79	Baud rate (Setting value for each connected sensor)	R/W	UINT	2	0: No connection 1: 96 kbps 2: 19.2 kbps 3: 38.4 kbps 4: 57.6 kbps 5: 115.2 kbps 6: 230.4 kbps 7: 312.5 kbps 8: 468.75 kbps 9: 500 kbps 10: 625 kbps 11: 833.3 kbps 12: 937.5 kbps			
						13: 1250 kbps			
129	0x81	Amplifier product code	RO	UINT	2	0x2503			

* RO=Read Only, R/W=Read/Write, WO=Write Only

OPTEX FA CO., LTD.

[Headquarters]

91 Chudoji-Awata-cho Shimogyo-ku Kyoto 600-8815 JAPAN

TEL +81-75-325-1314 FAX +81-75-325-2936



Digital Fiber Amplifier

D3RF Series

OPTEX FA CO. LTD.

Module Address

Address No.		Name	Access*	Format	Length	Value/Range	
dec	hex		Access*	(Offset)	(Byte)	value/Range	
1	0x1	Product Type	RO	UINT	2	0x0012	
2	0x2	Firmware Version	RO	UINT	2	0x0200 or more	
3	0x3	Protocol Version	RO	UINT	2	0x0001	
4	0x4	Product Revision	RO	UINT	2	0x0001	
5	0x5	Vendor Name	RO	STRING	2 to 16	"OPTEX FA"	
6	0x6	Product Name	RO	STRING	2 to 32	Product name	
7	0x7	Product ID	RO	STRING	2 to 16	Product ID	
8	0x8	User Tag Name (Max. 32 bytes)	R/W	STRING	2 to 32	""	
9	0x9	Operation Status	RO	UINT	2	0: Initialize 1: Idle 2: Execute 3: For use by user	
98	0x62	Error Code Refer to the separate "Errors" table.	R/W	UINT	2	Read: Last error code Write arbitrary value: Delete last error code	
100	0x64	Display	R/W	UINT	2	0: Numeric display 1: Bar display 2: Percentage display	
102	0x66	Teach-in mode CH1	R/W	UINT	2	0: 2-point teaching 1: 1-point teaching 2: Through teaching	
103	0x67	Teach-in mode CH2	R/W	UINT	2	3: Zone teaching 4: Auto-teaching 5: Percent teaching 6: Zero percent teaching	
104	0x68	Response Speed	R/W	UINT	2	0:1-HS (22 µs) 1:2-FS (85 µs) 2:3-ST (250 µs) 3:4-LG (1 ms) 4:5-PL (2 ms) 5:6-UL (4 ms) 6:7-EL (8 ms)	
105	0x69	Gain	R/W	UINT	2	0: Low power 1: Medium power 2: High power	
106	0x6A	MF-Input	R/W	UINT	2	0: External teaching input 1: Test input 2: Synchronous input 3: Counter reset input 4: External all teaching input 5: No function	
107	0x6B	Key Lock	R/W	UINT	2	0: Unlock 1: Lock level 1 (Full lock) 2: Lock level 2 (Half lock)	
108	0x6C	Operation mode CH1	R/W	UINT	2	0: L-on (Light ON)	
109	0x6D	Operation mode CH2	R/W	UINT	2	1: D-on (Dark ON)	
110	0x6E	Threshold level CH1 Lower Limit	R/W	UINT	2	-999 to 9999 The writable range varies depending on the operation mode.	
111	0x6F	Threshold level CH1 Upper Limit	R/W	UINT	2	-999 to 9999 Accessible only in Zone teaching mode.	
112	0x70	Threshold level CH2 Lower Limit	R/W	UINT	2	-999 to 9999 The writable range varies depending on the operation mode.	
113	0x71	Threshold level CH2 Upper Limit	R/W	UINT	2	-999 to 9999 Accessible only in Zone teaching mode.	

Addres	s No.	NI		Format	Length	Value (Deeper
dec	hex	Name	Access*	(Offset)	(Byte)	Value/Range
114	0x72	Timer setting CH1	R/W	UINT	2	0: OFF delay timer
115	0x73	Timer setting CH2	R/W	UINT	2	1: One-shot timer
116	0x74	CH1 Off delay time	R/W	UINT	2	
117	0x75	CH1 On delay time	R/W	UINT	2	0~9999:0~9999ms
118	0x76	CH2 Off delay time	R/W	UINT	2	-1~-9:0.1~0.9ms
119	0x77	CH2 On delay time	R/W	UINT	2	
123	0x7B	Hysteresis	R/W	UINT	2	1~40
125	0x7D	ASC (Automatic Sensitivity Control)	R/W	UINT	2	0: None 1: Normal correction 2: High-speed correction 3: Max. speed correction
126	0x7E	Energy Saving	R/W	UINT	2	0: Standard 1: Turn off display automatically 2: Emission cycle × 2 3: Display and emission cycle
127	0x7F	Reverse display	R/W	UINT	2	0: Standard 1: Inverted display
200	0xC8	Store zero-reset	WO	Byte	2	0 to 65535: Execute
201	0xC9	Cancel zero-reset	WO	Byte	2	0 to 65535: Execute
202	0xCA	Teach-in 1-point	WO	UINT	2	1: Output 1 teaching
203	0xCB	Teach-in 2-point	WO	UINT	2	2: Output 2 teaching
210	0xD2	Warm start Reset	WO	UINT	2	3: Restart
211	0xD3	Return to top menu	WO	Byte	2	Execute with written value
212	0xD4	Factory Reset	WO	UINT	2	3: Initialize (Allowed only when unlocked)

* RO=Read Only, R/W=Read/Write, WO=Write Only

Errors

High-order by	yte	Low-order by	/te	Paparintian
dec	hex	dec	hex	Description
0	0x0	0	0x0	No error
Appliaghlag	ddrooo numbor	1	0x1	The address number is out of range.
Applicable address number		2	0x2	The sub address number is not supported.
		3	0x3	An attempt was made to write setting values to sensors that are all locked, or to initialize while locked.
0	0x0	4	0x4	The teaching target output specification value (1 or 2) is incorrect.
0	UXU	5	0x5	Teaching mode has no second teaching, or the first has not yet been executed.
		6	0x6	The execution specification value (3) for restart and initialization is incorrect
		7	0x7	An attempt was made to write a setting value that is out of range.
Applicable a	address number	8	0x8	The setting value write operation failed. (Attempted to write a read-only setting value)
		9	0x9	Teaching to output 2 was attempted while output 2 was in counter mode.
		11	0xB	The amount of light received during teaching was too low.
		12	0xC	The amount of light received during teaching was saturated.
0	0x0	13	0xD	There was too little difference in the amount of light received during 2-point teaching.
		14	0xE	A teaching operation other than auto-teaching was attempted in differential operation mode.
		15	0xF	A hardware error was detected.

OPTEX FA CO., LTD.

[Headquarters]

91 Chudoji-Awata-cho Shimogyo-ku Kyoto 600-8815 JAPAN

TEL +81-75-325-1314 FAX +81-75-325-2936



White LED Digital Fiber Amplifier

D3WF Series

OPTEX FA CO. LTD.

Module Address

Addres	s No.			Format	Length	
dec	hex	Name	Access*	(Offset)	(Byte)	Value/Range
1	0x1	Product Type	RO	UINT	2	0x0012
2	0x2	Firmware Version	RO	UINT	2	0x0100
3	0x3	Protocol Version	RO	UINT	2	0x0001
4	0x4	Product Revision	RO	UINT	2	0x0001
5	0x5	Vendor Name	RO	STRING	2 to 16	"OPTEX FA"
6	0x6	Product Name	RO	STRING	2 to 32	Product name
7	0x7	Product ID	RO	STRING	2 to 16	Product ID
8	0x8	User Tag Name (Max. 32 bytes)	R/W	STRING	2 to 32	User ID (Max. 32 characters)
9	0x9	Operation Status	RO	UINT	2	0: Initialize 1: Idle 2: Execute 3: For use by user
98	0x62	Error Code Refer to the separate "Errors" table.	R/W	UINT	2	Read: Last error code Write arbitrary value: Delete last error code
100	0x64	Display	R/W	UINT	2	0: Numeric display 1: Bar display 2: Percentage display
102	0x66	Teach-in mode CH1	R/W	UINT	2	0: 1-point teaching
103	0x67	Teach-in mode CH2	R/W	UINT	2	1: 2-point teaching 2: Dynamic teaching
104	0x68	Response speed	R/W	UINT	2	0∶16 μs 1∶200 μs
105	0x69	Gain	R/W	UINT	2	0: Low power 1: Standard power 2: High power 3: Automatic power
106	0x6A	MF Input	R/W	UINT	2	0: Extended input 1: All teaching (master only) 2: Light ON/Dark ON switching 3: Test input 4: Off
107	0x6B	Key Lock	R/W	UINT	2	0: Cancel 1: Lock 2: Lock for non-extended input
108	0x6C	Operation mode CH1	R/W	UINT	2	0: Auto
109	0x6D	Operation mode CH2	R/W	UINT	2	1: L-on (Light ON) 2: D-on (Dark ON)
110	0x6E	Threshold level CH1 Lower Limit	R/W	UINT	2	-999 to 9999
112	0x70	Threshold level CH2 Lower Limit	R/W	UINT	2	The writable range varies depending on the operation mode.
114	0x72	Timer setting CH1	R/W	UINT	2	0: Delay OUT
115	0x73	Timer setting CH2	R/W	UINT	2	1: Individual
116	0x74	CH1 Off delay time	R/W	UINT	2	
117	0x75	CH1 On delay time	R/W	UINT	2	0~9999:0~9999ms
118	0x76	CH2 Off delay time	R/W	UINT	2	-1~-9:0.1~0.9ms
119	0x77	CH2 On delay time	R/W	UINT	2	
121	0x79	Synchronization	R/W	UINT	2	0: Asynchronous 1: Synchronous
123	0x7B	Sensitivity	R/W	UINT	2	0:10 % 1:20 % 2:50 %

Address No.		Name	Access*	Format	Length	Value/Dance
dec	hex	name	Access	(Offset)	(Byte)	Value/Range
125	0x7D	ASC (Automatic Sensitivity Control)	R/W	UINT	2	0:OFF 1:ON
126	0x7E	Eco mode	R/W	UINT	2	0:OFF 1:ON
127	0x7F	Reverse display	R/W	UINT	2	0: Normal display 1: Inverted display
200	0xC8	Store zero-reset	WO	Byte	2	Execute with write operation
201	0xC9	Cancel zero-reset	wo	Byte	2	Execute with write operation
202	0xCA	Teach-in 1-Point	WO	UINT	2	1: Output 1 teaching
203	0xCB	Teach-in 2-Point	WO	UINT	2	2: Output 2 teaching
210	0xD2	Warm start Reset	WO	UINT	2	3: Restart
211	0xD3	Return to top menu	WO	Byte	2	Execute with write operation
212	0xD4	Factory Reset	WO	UINT	2	3: Initialize (Allowed only when unlocked)

* RO=Read Only, R/W=Read/Write, WO=Write Only

Errors

High-order	byte	Low-order byte		Description	
dec	hex	dec	hex	Description	
0	0x0	0	0x0	No error	
Applicable address		1	0x1	The address number is out of range.	
nu	umber	2	0x2	The sub address number is not supported.	
		3	0x3	An attempt was made to write setting values to sensors that are all locked, or to initialize while locked.	
		4	0x4	The teaching target output specification value (1 or 2) is incorrect.	
0	0x0	5	0x5	Teaching mode has no second teaching, or the first has not yet beer executed.	
		6	0x6	The execution specification value (3) for restart and initialization is incorrect.	
Annling		7	0x7	An attempt was made to write a setting value that is out of range.	
	Applicable address number		0x8	The setting value write operation failed. (Attempted to write a read- only setting value)	
		11	0xB	The amount of light received during teaching was too low.	
		12	0xC	The amount of light received during teaching was saturated.	
0	0x0	13	0xD	There was too little difference in the amount of light received during point teaching.	
		15	0xF	A hardware error was detected.	

OPTEX F	A CO., LTD.
----------------	-------------

[Headquarters]

91 Chudoji-Awata-cho Shimogyo-ku Kyoto 600-8815 JAPAN

TEL +81-75-325-1314 FAX +81-75-325-2936



Through-beam Edge Sensor

TD1 Series

OPTEX FA CO. LTD.

Module Address

ddress No		Name	Access	Format	Length	Value/Range	Address for
ec	hex		*1	(Offset)	(Byte)		CDA*2
1	0x1	Product Type	RO	UINT	2	0x0011	
2	0x2	Firmware Version	RO	UINT	2	0x1011	
3	0x3	Protocol Version	RO	UINT	2	0x0	
4	0x4	Product Revision	RO	UINT	2	Product hardware version	
5	0x5	Vendor Name	RO	STRING	2 to 16	"OPTEX FA"	
6	0x6	Product Name	RO	STRING	2 to 32	Product name	
7	0x7	Product ID	RO	STRING		Product ID	
8	0x8	User Tag Name (Max. 32 bytes)	R/W	STRING		User ID (Max. 32 characters)	
						0: Initialize	
						1: Idle	
9	9 0x9	Operation Status	RO	UINT	2	2: Execute	
						3: For use by user	
14	0xE	Serial Number	RO	STRING	2 to 32	Set using ASCII code	
		Error Status				Read: Last error code	
98	0x62	Refer to the separate "Errors" table.	R/W	UINT	2	Write arbitrary value: Delete last error code	
100	0x64	Threshold Near	R/W	UINT	2	· · · · · · · · · · · · · · · · · · ·	
100	0x64 0x65	Threshold Far	R/W	UINT	2	-32768~32767	~
102	0x65 0x66		R/W	UINT	2	0~32767	~
102	0000	Output hysteresis	R/W	UINT	2		~
103	0x67	Calculation flags	R/W	UINT	2	0: No calculation	~
		*		-		1: With calculation	
104	0x68	Calculation coefficient Add (Setting value for each	R/W	UINT	2		
		connected sensor)				-10000~10000	~
105	0x69	Calculation coefficient Multiply (Setting value for each	R/W	UINT	2	10000 10000	•
100	0,000	connected sensor)		0	-		
106	0x6A	Calculation coefficient Divide (Setting value for each	R/W	UINT	2	1~32767	~
100	UXOA	connected sensor)	R/W	UINT	2	1~32101	v
107	0x6B	Monitor accumulations	R/W	UINT	2	0: Sensor 2 measured value	~
107	UXOD		N/W	UINT	2	1: Calculated value	~
108	0x6C	Calculation Values Head1	R/W	UINT	2	0: None	
109	0x6D	Calculation Values Head2	R/W	UINT	2	1: Addition	
110	0x6E	Calculation Values Unit left Head1	R/W	UINT	2	2: Subtraction	~
111	0x6F	Calculation Values Unit left Head2	R/W	UINT	2	3: Absolute value difference	
						0:PNP (N.O.)	
				UINT		1:NPN (N.O.)	
112	0x70	I/O polarity	R/W		2	2:PNP (N.C.)	~
						3:NPN (N.C.)	
						0: No output	
113	0x71	Output Selection CH1	R/W	UINT	2	1: Calculated value within threshold range (GO)	
						2: Calculated value out of range on near side (LO)	
114	0x72	Output Selection CH2	R/W	UINT	2	3: Calculated value out of range on far side (HI)	
						4: Sensor 1 within threshold range (GO)	
						5: Sensor 1 out of range on near side (LO)	~
						6: Sensor 1 out of range on far side (HI)	
115	0x73	Output Selection CH3	R/W	UINT		7: Sensor 2 within threshold range (GO)	
						8: Sensor 2 out of range on near side (LO)	
						9: Sensor 2 out of range on far side (HI)	
						0: No function	
						1: Teaching (Far side on rising edge, near side on falling	
						edge)	
116	0x74	External input selection	R/W	UINT	2	2: BGS/FGS teaching	~
						3: Zero reset	
						4: Laser emission OFF	
					1	0: None	
					1	1: Calculated value	
117	0x75	Analog output selection	R/W	UINT	2	2: Sensor 1	~
						3: Sensor 2	
	1		1		1	0: No scaling	
		Analog scaling		UINT	2		~
118	0x76	Analog scaling	R/W		2	1: Scaling	
118 119	0x76	Analog Scaling (max.) 10 V / 20 mA	R/W	UINT	2	1: Scaling -32768~32767	~

Address N	lo.	Name	Access	Format	Length	Value/Range	Address fo
lec	hex	Inallie	*1	*1 (Offset) (value/Range	CDA*2
121	0x79	Baud rate (Setting value for each connected sensor)	R/W	UINT	2	0: No connection 1: 9.6 kbps 2: 19.2 kbps 3: 38.4 kbps 4: 57.6 kbps 5: 115.2 kbps 6: 230.4 kbps 7: 312.5 kbps 8: 468.75 kbps 9: 500 kbps 10: 625 kbps 11: 833.3 kbps 12: 937.5 kbps 13: 1250 kbps	v
129	0x81	Amplifier product code	RO	UINT	2	0x2503	~
134	0x86	Average Number	R/W	UINT	2	1~128	
135	0x87	Measurement Type	R/W	UINT	2	0: Edge positive 1: Edge negative 2: Width	
136	0x88	Sampling rate	R/W	UINT	2	0:500µs (Fixed value)	
143	0x8F	Measuring direction	R/W	UINT	2	0:Top to Bottom 1:Buttom to Top	
144	0x90	Zeroing value	RO	UINT	2	-9999~5000	
152	0x98	Sensitivity	R/W	UINT	2	0: Minimum value 1: Second value 2: Third value 3: Fourth value 4: Maximum value 5: Adjustment value	
200	0xC8	Save zero-reset	WO	Byte	2	Execute with write operation	
201	0xC9	Cancel zero-reset	WO	Byte	2	Execute with write operation	
212	0xD4	Factory Reset	WO	UINT	2	3: Initialize	

*1. RO=Read Only, R/W=Read/Write, WO=Write Only

*2. Displacement Sensor Amplifier Unit

Errors

High-order byte	ligh-order byte			Description	
dec hex		dec	hex	Description	
0	0x0	0 0x0		No error	
		1	0x1	The address number is out of range.	
	2 0x2		0x2	The sub address number is not supported.	
		3	0x3	The ETX code in the command string is invalid.	
		4	0x4	A checksum error was detected.	
Applicable add	fress number	5	0x5	The command code is invalid.	
			0x6	A non-regulated parameter was specified.	
		7	0x7	An out of range numeric value was specified.	
		8	0x8	Description of the second seco	
		9	0x9	Reserved	

OPTEX FA CO., LTD.

[Headquarters]

91 Chudoji-Awata-cho Shimogyo-ku Kyoto 600-8815 JAPAN

TEL +81-75-325-1314 FAX +81-75-325-2936



INSTRUCTION MANUAL

0809900

IO-Link GATEWAY

UC2-IOL

Download the index list, the IO-Link setting file (IODD file) and the connected module operation manual from the Optex FA website. https://www.optex-fa.com

OIO-Link (

OPTEX FA CO..LTD.

- Thank you for purchasing this IO-Link Gateway UC2-IOL
- Before using this product, please read this manual carefully to ensure proper use
- Read this manual throughly, and then keep this manual at hand so that it can be used whenever necessary.
 The warranty period of this product is one year after delivery. However, any fault attributable to natural disasters or any other similar disasters or modification or repair will be excluded from the scope of the warranty.

Safety Precautions

Safety precautions for ensuring safe operation of this product are displayed as follows with the following symbols

Precautions listed here describe important information about safety. Make sure to follow them accordingly.

Safety Symbols

Indicates that any improper operation or handling may result in moderate or minor injury, and in rare cases, serious injury or death. Also indicates a risk of serious property damage.
Indicates that any improper operation or handling may result in minor injury or property damage.

	Do not disassemble, repair, modify, deform under pressure, or attempt to incinerate this product. Doing so may cause injury or fire.
A	This product is not explosion-proof and should not be used around flammable or explo- sive gases or liquids. Doing so may cause ignition resulting in an explosion or fire.
$\mathbb{A}\mathbb{A}$	Do not use air dusters or any spray that uses flammable gas around the product or on the inside of the product. Doing so may cause ignition resulting in an explosion or fire.
	 Do not install this product in any of the following locations. Doing so may cause a fire, damage, or a malfunction. 1. Locations where dust, salt, iron powders, or vapor (steam) is present. 2. Locations subjected to corrosive gases or flammable gases. 3. Locations where oil or chemical splashes may occur. 4. Locations where heavy vibrations or impacts may occur. 5. Locations where the ambient temperature exceeds the rated range. 6. Locations with strong electric or magnetic fields. 8. Outdoor locations or locations subject to direct light.
\bigcirc	This is a class A product. In a domestic environment this product may cause radio inter- ference, in which case the user may be required to take adequate measures.
\bigcirc	This product is not intended for use with nuclear power, railways, aviation, vehicles, medical equipment, food-handling equipment, or any application where particular safe- ty measures are required. Absolutely do not use this product for any of these fields.
\bigcirc	This product cannot be used in applications that directly or indirectly detect human bodies for the purpose of ensuring safety. Do not use this product as a detection device for protecting the human body.
0	What to do in the event of a malfunction such as smoke being emitted from the product If you detect any malfunction including emission of smoke, abnormal smells or sounds, or the body becoming very hot, immediately stop operating the product and turn off the sensor

power. Failure to do so may cause a fire. Repairing the product is dangerous and should in no way be performed by the customer. Contact an OPTEX FA sales representative for repairs.

CAUTION

- Make sure to turn the power off before wiring the cable or connecting/disconnecting the connector Connecting or disconnecting while energized may damage the product or cause electric shock.
 Do not wire with high voltage cables or power lines. Doing so may cause malfunction or damage by induction
- Do not bend the cable when below the freezing point. This may cause the cable to break.
- Do not drop the product or subject the product to strong impacts. Doing so may damage the product.
 Follow the instructions in this manual or the specified instruction manual when wiring the product or the dedicated controller for the correct wiring method. Incorrect wiring can damage the product or the controller. or cause a malfunction
- When disconnecting the connector, be careful not to touch the terminals inside the connector, and do not allow foreign objects to enter the connector.
- Install this product as far away as possible from high-voltage equipment, power equipment, equipment tha generates large switching surges, inverter motors, welders, or any equipment that can be a source of noise. When connecting or disconnecting the cable, make sure to hold it by the connector portion, and do not appl excessive force to the cable.

NOTICE

- After carefully considering the intended use, required specifications, and usage conditions, install and use the
 product within the specified ranges.
- All specifications may be changed without notice.
 When using this product, it is the responsibility of the customer to ensure necessary safety designs in hard-ware, software, and systems in order to prevent any threat to life, physical health, and property due to product malfunction or failure.
- Do not use this product for the development of weapons of mass destruction, for military use, or for any
- Do hor use this product for the development of weapons of mass destruction, for military use, or for any
 other military applications. Moreover, if this product is to be exported, comply with all applicable export
 laws and regulations, including the "Foreign Exchange and Foreign Trade Act" and the "Export Administration Regulations," and carry out the necessary procedures pursuant to the provisions therein.
 For more details on conformity to the Restriction of Hazardous Substances Directive for this product, please
 contact an OPTEX FA sales representative. Before using this product, fully examine the applicable environmental laws and regulations, and operate the product in conformity to such laws and regulations. OPTEX FA
 desenate environmental laws and regulations. does not assume any responsibility for damages or losses occurring as a result of noncompliance with appli cable laws and regulations

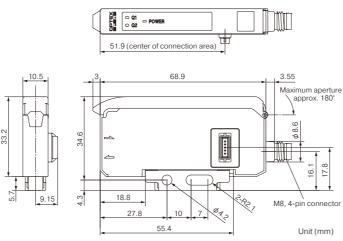
1. Included Accessories

Instruction manual (this manual)

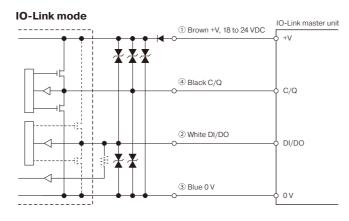
Mounting bracket

2. Dimensions

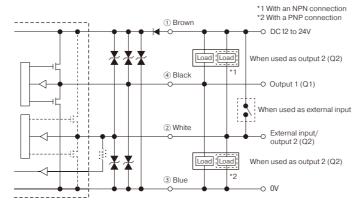
UC2-IOL (with mounting bracket equipped)



3. I/O Circuit Diagrams



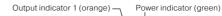
SIO mode (standard I/O mode) with push-pull

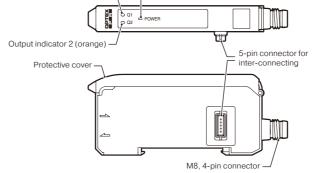


M8 connector pin numbers



4. Part Names





5. Lists of Cables and Connectable Models

Connector cables

(For UC2-IOL and D3RF/D3WF series connector types)

Series name	Model	Туре
M84CN series	M84CN-2S	2 m long
(M8, 4-pin connector cable)	-5S	5 m long
*This is required when four or more D3RF/D3WF	- 10S	10 m long
connector type master units are inter-connected.		

Fiber amplifiers

Series name	Model	Туре
D3RF series (red light	D3RF-TMN	Inter-connection master unit, cable type
source)	TMP	cable type
	-TMCN4	connector type
	-TMCP4	connector type
	-TDMN	cable type, two outputs
	-TDMP	cable type, two outputs
	D3RF-TSN	Inter-connection slave unit, cable type
	-TSP	cable type
	-TSCN4	connector type
	-TSCP4	connector type
	-TDSN	cable type, two outputs
	-TDSP	cable type, two outputs
D3WF series (white light	D3WF-TMCN4	Inter-connection master unit, connector type
source)	D3WF-TSCN4	Inter-connection slave unit, connector type

Displacement sensor amplifier units

Series name	Model	Туре
CDA series	CDA-M -DM2	Inter-connection master unit
	CDA-S	Inter-connection slave unit

Displacement sensors/edge sensors

(Use the CDA series to connect these sensors.)

Series name/ product name	Model	Туре
CD22 series	CD22-15-485M12	RS-485 communication type
(laser displacement	-35-485M12	*DSL-1204-G02M connector cable is required to
sensor)	- 100-485M 122	connect to the CDA.
Amplifier connection cable	DSL-1204-G02M	Cable for connecting to the CDA, 2 m long
TD1 series (through-beam edge sensor)	TD1-010M8	
CD22/TD1 extension cable	DSL-0804-G02M	Sensor head extension cable, 2 m long
	-G05M	Sensor head extension cable, 5 m long

*These are lists of connectable models as of January 2020. There are plans to add products sequentially, so contact OPTEX FA for details on models that are not listed here

6. Installation

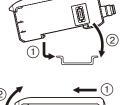
Installation on DIN rail or the included mounting

bracket

Hook the indicator-side tab on the DIN rail or the included mounting bracket (1), and then press down until the hook locks (2).

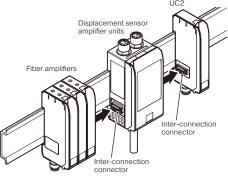
Removal

While pressing the main unit in the direction of (①), lift the indicator side (2) to remove the main unit.



Inter-connecting with fiber amplifiers and displacement sensor amplifier units

To inter-connect all the units, slide the fiber amplifiers and displacement sensor amplifier units along the DIN rail that the UC2 is attached to.

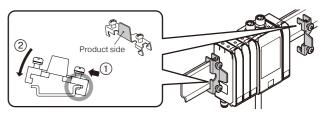


- Be sure to turn off the power before performing this work.
- When inter-connecting, check the ambient temperature and use the units within the operating range for the ambient temperature.
- · To prevent short-circuits, be sure to attach the included protective caps for connector types of fiber amplifiers to connecting terminals that are not in use. For cable types, prevent lead wires that are not in use from connecting to the other terminals by, for example, cutting these lead wires and wrapping them individually with insulating tape.
- · Do not remove these units/amplifiers from the DIN rail while multiple units are inter-connected.

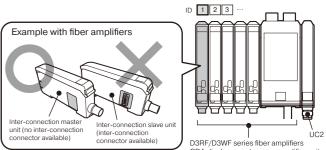
Installing end plates (sold separately)

To prevent the inter-connected units from coming loose, attach end plates (sold separately; model: BEF-EB01-W190) to the DIN rail so that they surround the inter-connected units and fix the end plates in place with screws.

Orient the end plates so that the product side is against the unit and the fiber amplifiers, making sure to attach the left and right sides in opposite orientations. Attach the end plates by hooking the notched side on the DIN rail first, as shown in the following diagram. Use a tightening torgue of 0.9 N·m or less.



When inter-connecting the unit to D3RF/D3WF series fiber amplifiers and to CDA series displacement sensor amplifier units, be sure to connect the inter-connection master unit on the left end (ID = 1). If an inter-connection slave unit is connected on the left end, the inter-connection connector will be exposed, causing interference with the end plate. Also, fouling on or damage to the inter-connection connector may damage the fiber amplifiers

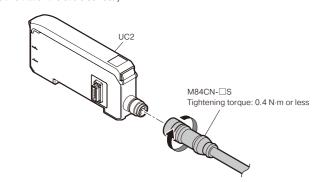


CDA displace ent sensor amplifier unit

7. Connecting

Connecting the connector cable (sold separately)

Use M84CN- S M8 connector cable to connect to the IO-Link master unit. Check the orientation of the connector pins on the unit side and on the connector cable side and attach the cable correctly.

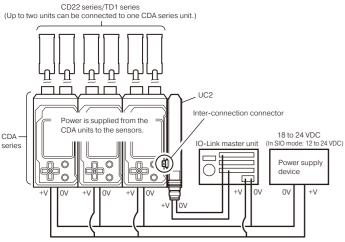


The UC2 operates with a supply voltage of 18 to 24 VDC (12 to 24 VDC in SIO mode) Be sure to check the wiring to the IO-Link master unit and to the power supply device because incorrect wiring may cause a fire or damage the product.

Connecting UC2 to CDA units (connecting to CD22 and TD1 units)

To connect the UC2 to CD22 series laser displacement sensors and to TD1 series throughbeam edge sensors, connect through CDA series displacement sensor amplifier units

<Connection example>



Supply power to the UC2 from the IO-Link master unit. Supply power to the inter-connected CDA units from the same power supply device as the IO-Link master unit.

- · The CDA series power supply must be connected to a power supply that is separate from that for the
- UC2 and is the same as that for the IO-Link master unit.
 Power is supplied to the CD22 series and to the TD1 series from the CDA series. The UC2 can be
- inter-connected to CDA series master units and slave units. When inter-connecting the UC2 to the CDA series, be sure to inter-connect the inter-connection mas-ter unit on the left end. If an inter-connection slave unit is connected on the left end, the inter-connec-
- tion connector will be exposed, damaging the product.
 Up to eight CDA series units can be inter-connected. Depending on the settings, all the output information and distance information of up to 7 sensors or all the output information of up to 16 sensors and the distance information of up to 5 sensors can be acquired.
- Insure that the length of the power cable to the unit and to the CDA series is 20 m or less. The operating temperature of the CDA series slave unit (CDA-S) and the load current of the control with the constructed macter unit (CDA M CDA DM2) as below.

<pre>output are restricted by the c <cda-m+cda-s></cda-m+cda-s></pre>	connected master unit (CDA-M, CD/	A-DM2) as below.
Operating temperature	-25 to +50°C	
Load current of control output		Up to 100mA per output channel (Up to 2A including self-consumption)
Number of connected units	1 unit (Only CDA M)	O to O unito

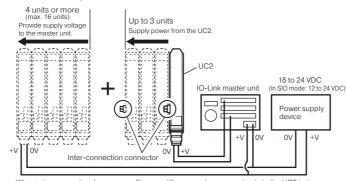
Number of connected units	1 unit (Only CDA-M)	2 to 8 units
<cda-dm2+cda-s></cda-dm2+cda-s>		
Operating temperature	-25 to +50°C	-20 to +45°C
Load current of control output	Up to 100mA per output channel	Up to 20mA per output channel
Number of connected units	1 unit (Only CDA-DM2)	2 to 8 units

Connecting UC2 to D3RF/D3WF series

Use the inter-connection connectors to connect the UC2 to D3RF/D3WF series fiber amplifiers.

Connecting power supply wires

When three or less fiber amplifiers are inter-connected to the UC2, the fiber amplifiers operate with the power supplied from the inter-connection connector of the UC2. When four or more fiber amplifiers are inter-connected to the UC2, connect the inter-connection master unit on the left end to the same power supply as the IO-Link master unit.

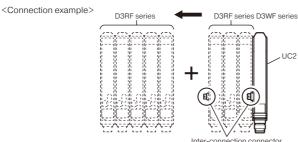


When inter-connecting four or more fiber amplifiers, supply power not only to the UC2 but also to the fiber amplifier master unit on the left end. Also, supply power to the fiber amplifier master unit and the IO-Link master unit from the same power supply device

Fiber amplifier inter-connection order

<Connection example>

When inter-connecting the unit to D3RE series and D3WE series fiber amplifiers, do so with all the D3WF units closest to the unit and all the D3RF units farthest away.

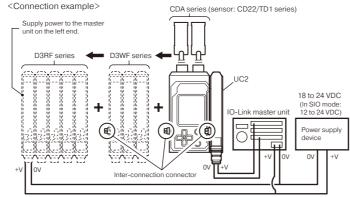


- When inter-connecting the unit to fiber amplifiers, be sure to connect an inter-connection master unit on the left end. If an inter-connection slave unit is connected on the left end, the inter-connection connector will be exposed, damaging the product.
- A total of up to 16 D3RF series and D3WF series fiber amplifiers can be inter-connected. Depending
 on the settings, all the output information and receiving light level information of up to 7 sensors or all the output information of up to 16 sensors and the receiving light level information of up to 5 sensors can be acquired
- Ensure that the length of the power cable to the unit and to the fiber amplifiers is 20 m or less When using a cable type fiber amplifier, perform insulation processing to prevent the power supply wires (brown/blue) from short-circuiting. Short-circuits may lead to device damage. Furthermore, in the case of connector type units, connect the included black cap to the M8 connector when the con-nector cable is not connected to prevent the pins from short-circuiting.
- The operating temperature and the load current of control output are restricted by the number of connected units as below:

No. of connected unit	1 to 3 unit(s)	4 to 8 units	9 to 16 units
Operating temperature	-25 to +55°C	-25 to +50°C	-25 to +45°C
Load current of control output	100 mA	50 mA	20 mA

Connecting UC2 to a combination of CDA series and D3RF/D3WF series units

When inter-connecting the unit to CDA displacement sensor amplifier units and to D3RF/ D3WF series fiber amplifiers, do so with all the CDA units closest to the unit, all the D3WF units next closest to the unit, and all the D3RF units farthest away.

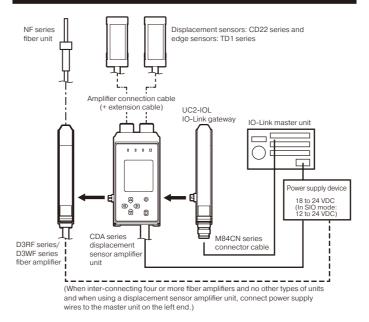


Supply power to the UC2 from the IO-Link master unit. Furthermore, supply power not only to the UC2 but also to the fiber amplifier master unit on the left end.

Connect the fiber amplifier master unit and the IO-Link master unit to the same power supply device · For the CDA, use an inter-connection slave unit.

- Supply power to the CDA series and D3RF/D3WF series units from the same power supply as that for the IO-Link master unit.
 When inter-connecting the UC2 to the CDA series, be sure to inter-connect the inter-connection mas-
- Another the left end. If an inter-connection slave unit is connected on the left end, the inter-connection connected on the left end, the inter-connection connector will be exposed, damaging the product.
 A total of up to 16 D3RF/D3WF series and CDA series units can be inter-connected. (One CDA dis-
- placement sensor amplifier unit counts as two sensors.) Depending on the settings, all the output information and distance/receiving light level information of up to 7 sensors or all the output information of up to 16 sensors and the distance/receiving light level mation of up to 5 sensors can be acquired.
- Ensure that the length of the power cable to the unit and to the CDA series is 20 m or less.
 When using a cable type D3RF/D3WF series unit, perform insulation processing to prevent the power
- supply wires (brown/blue) from short-circuiting. Short-circuits may lead to device damage. Furthermore, in the case of connector type units, connect the included black cap to the M8 connector when the connector cable is not connected to prevent the pins from short-circuiting.

8. System Configuration Example



- The UC2 can be inter-connected to up to a total of 16 sensors. (One CDA displacement sensor amplifier unit counts as two sensors.) Depending on the settings, all the output information and distance/receiving light level information of up to 7 sensors or all the output information of up to 16 sensors and the distance/receiving light level information of up to 5 sensors can be acquired.
- . The power supply connections of the devices vary depending on the devices that are connected and on the number of devices that are connected. Check the information under "7. Connecting" and connect the power supplies correctly

9. Specifications

Madal		UC2-IOL	
10.111	Model		
IO-Link com-	Minimum cycle time	2.0 ms	
munication specifications	Baud rate	COM3 (230.4 kbps)	
	ISDU support	Available	
	IO-Link revision	1.1	
	Number of process input data bytes	16	
	Number of process output data bytes	0	
	IO-Link frame type	F-Sequence Type 2.V	
	IODD revision	1.1.0	
Connected	Connectable models	D3RF series/D3WF series inter-connection master unit	
devices		and slave unit	
		CDA series master unit and slave unit	
	Number of connectable units	Up to 16 (One CDA unit requires two spaces.)	
	Connection type	5-pin connector for inter-connection (functions as an	
		inter-connection end unit)	
Indicators		Power indicator (green), SIO: lit at all times, IO-Link: flashing	
		Output indicator × 2 (orange)	
Ratings	Supply voltage	SIO: 12 to 24 Vdc including 10% of ripple (P-P) IO-Link: 18 to 24Vdc including 10% of ripple (P-P) (Class 2 or SELV+LIM)*1	
	Current consumption	40 mA max.*2	
Control output	our off off off off off	Push-Pull × 2 outputs, max. 100 mA/24 VDC (total for	
Control output		2 outputs)	
		Residual voltage: 1.8 V or less	
External input		Teaching input, smart task input	
Connection typ	e	Connector type: M8, 4-pin	
Warm-up time		With 1 D3RF/D3WF unit inter-connected: 300 ms, with 16 D3RF/D3WF units inter-connected: 350 ms CDA + CD22: 2.5 s CDA + CD1: 350 ms	
Protection circu	uit	Reverse connection protection, overcurrent protection	
Environmental	Installation location	Indoor use	
resistance	Operating temperature/	-25 to +55°C (no freezing)/35 to 85%RH (no con-	
	humidity	densation)	
	Storage temperature/humidity	-40 to +70°C (no freezing)/35 to 95%RH (no con- densation)	
	Vibration resistance	10 to 55 Hz; double amplitude 1.5 mm; 2 hours in each of the X, Y, and Z directions	
	Shock resistance	Approx. 50 G (500 m/s ²); 3 times in each of the X, Y, and Z directions	
	Degree of protection	IP50 (According to IEC 60529)	
	Pollution degree	2 or less	
	Operating altitude	2000 m or less	
Applicable regulations	FMC	EMC directive (2014/30/EU)	
	Environment	RoHS directive (2011/65/EU), China RoHS (MIIT Order No. 32)	
Applicable standard		EN 60947-5-2	
Company standards		Noise resistance: Feilen Level 3 cleared	
Weight		Approx. 16 g	
Material Included accessories		Housing and protective cover: PC Instruction manual, mounting bracket	
Included access	corios	Instruction manual mounting bracket	

- Voltage) and the LIM circuit (Limited Energy Circuit)
- *2. Not including control output load curre

https://www.optex-fa.com

91 Chudoji-Awata-cho Shimogyo-ku Kyoto 600-8815 JAPAN

TEL+81-75-325-1314 FAX +81-75-325-2936

Support for the China RoHS directive

[Headquarters]

This device complies with part 15 of the FCC Rules.



undesired operation

the following websit

For details on the support for the China RoHS directive (the Administrative Measure on the Control of Pollution Caused by Electronic Information Products), see

OPTEX FA CO., LTD.

equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

Operation is subject to the following two conditions: (1) This device may not cause harmful interference and (2) this device must accept any interference received, including interference that may cause

This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this

